

Docket No.: S1022.81055US00
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Luc Wuidart
Serial No.: 10/694,165 Patent No. 7,352,606
Filed: October 27, 2003 Issued: April 1, 2008
Patent No.: 7,352,606
For: MONOTONIC COUNTER USING MEMORY CELLS

Examiner: V. T. T. Nguyen
Art Unit: 2824 Confirmation No. 3053

Certificate of Electronic Filing Under 37 CFR 1.8

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being transmitted via the Office electronic filing system in accordance with § 1.6(a)(4).

Dated: April 7, 2008

_____/Gail Driscoll/

REQUEST FOR CERTIFICATE OF CORRECTION PURSUANT TO 37 CFR 1.322

Attention: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Upon reviewing the above-identified patent, Patentee noted typographical errors which should be corrected.

The word "monotonous" found in the abstract, claims and throughout the patent was changed to --monotonic-- in a Preliminary Amendment filed on October 27 2003 as part of the original application . Other changes made in the preliminary amendment include changing the word "memorization" to "storage" and adding the word "the" before the word "same" in the first line of the Detailed Description.

The amendments submitted in the preliminary amendment were not incorporated into the issued patent. Additionally, the word "monotonic" was misspelled in claim 17. The misspelling did not occur in the claim when it was submitted to the Patent Office in the amendment filed on May 29, 2007.

Accordingly, Patentee respectfully requests a Certificate of Correction be granted to make the following corrections to issued U.S. Patent No. 7,352,606.

In the Specification:

column 1, line 8 should read:

more specifically, to monotonic counters with an irrevers-

column 1, line 11 should read:

Currently, to form monotonic up-counters, fuse ele-

column 1, line 42 should read:

which would be likely to be used in monotonic counting

column 1, line 48 should read:

The present invention aims at providing a monotonic

column 1, line 57 should read:

form a monotonic counter, the counting of which is com-

column 1, line 60 should read:

of a monotonic counter by using conventional MOS-type

column 1, line 63 should read:

provides a monotonic counter formed as an integrated

column 1, line 65 should read:

containing at least one storage element formed of a

column 2, line 57 should read:

The same elements have been designated with the same reference

column 2, line 65 should read:

an irreversible monotonic counter is desired.

dolumn 2, line 66 should read:

A feature of the present invention is to form a monotonic

column 3, line 2 should read:

the storage element of each counting cell is formed of

column 9, line 45 should read:

forming a perfectly reliable monotonic counter, the pro-

column 9, line 55 should read:

monotonic counter. It should however be noted that,

column 10, line 22 should read:

resistive storage polysilicon element, programmable

In the Abstract:

line 1 should read:

A monotonic counter formed as an integrated circuit, each

line 3 should read:

at least one memorization element formed of a polysilicon

In the Claims:

column 12, claim 17, line 19 should read:

decreased, thereby programming the monotonic

The amendments made by Patentee byn Preliminary Amendment on October 27 2003 were not incorporated into the issued patent by the Patent Office. Therefore, the errors identified above were not the fault of Patentee, accordingly it is believed that no fee is required. However, should the Examiner determine a fee to be necessary, the fee should be charged to the deposit account of the undersigned, Deposit Account No. 23/2825.

Transmitted herewith is a proposed Certificate of Correction effecting such amendment.
Patentee respectfully solicits the granting of the requested Certificate of Correction.

Dated: April 7, 2008

Respectfully submitted,

By: 

James H. Morris

Registration No.: 34,681

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 3

PATENT NO. : 7,352,606
APPLICATION NO. : 10/694,165
ISSUE DATE : April 1, 2008
INVENTOR(S) : Luc Wuidart

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